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January 31, 2006

Date

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TRANSMITTAL  FORM  (to be used for all correspondence after initial filling)  Mail Stop: AMENDMENT			Application / Conf. No.	10/796,750 / 2526
			Filing Date	March 08, 2004
			First Named Inventor	Vasisht Mantra Vadi
			Examiner Name	Trong Q. Phan
			Art Unit	2827
Express Mail Receipt No.			Patent No.	
Total Number of Pages in This Submission			Attorney Docket Number	X-1292 US
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Affidavit(s)/declaration(s)		Licensing-related Papers  Petition -		Status Letter
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Response to Missing Parts under 37 CFR 1.52 or 1.53		-		
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Firm or Customer Number Attn: Kim Kanzaki		<b>24309</b> (Customer Number)	Reg. Number 37,652	
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Date January 31, 2006		Charge any additional fees required/credit any overpayment to our Deposit Account Number: 24-0040		
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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450 on this date:				
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X-1292 US 10/796,750 FEB 0 8 7006 PE

PATENT Conf. No.: 2526

## MATHE UNITED STATES PATENT OFFICE

Applicants:

Vasisht Mantra Vadi, et al.

Assignee:

Xilinx, Inc.

Title:

"Segmented Dataline Scheme in a Memory with Enhanced Full

Fault Coverage Memory Cell Testability"

Serial No.:

10/796,750

File Date:

March 8, 2004

Examiner:

Trong Q. Phan

Art Unit:

2827

Docket No.:

X-1292 US

Conf. No.:

2526

Mail Stop AMENDMENT COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450

## AMENDMENT IN RESPONSE TO FIRST OFFICE ACTION

Dear Sir:

In response to the First Office Action mailed October 31, 2005, please amend the above-identified application as indicated beginning on the following page.

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 3 of this paper.

Remarks begin on page 12 of this paper.